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APPLICANTS Katsura Hirai, Tokyo, JAPAN; <i>JE-U</i>				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/12140 09/24/2003 <i>JE-U</i>				
** FOREIGN APPLICATIONS ***** JAPAN 2002-278599 09/25/2002				
Foreign Priority-claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and <i>Elmer J. Ullrich</i> <i>EU</i> Acknowledged <u>Examiner's Signature</u> <u>Initials</u>		STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 74
INDEPENDENT CLAIMS 4				
ADDRESS 1933				
TITLE Electrical circuit, thin film transistor, method for manufacturing electric circuit and method for manufacturing thin film transistor				
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